

IN THE CLAIMS

Please amend the claims as follows.

- 1 1. (Original) A first apparatus comprising:
2 a non-volatile memory that contains:
3 (A) bus numbering information for at least one bus located within the first
4 apparatus; and
5 (B) bus numbering information for at least one bus located within a second
6 apparatus coupled to the first apparatus.

- 1 2. (Original) The first apparatus of claim 1 wherein the bus numbering
2 information comprises a beginning bus number and a number of buses.

- 1 3. (Original) The first apparatus of claim 1 wherein the non-volatile memory
2 comprises at least one identifier for determining if contents of the non-volatile
3 memory are valid.

1 4. (Original) A computer system comprising:
2 a first physical enclosure;
3 a second physical enclosure coupled to the first physical enclosure, the second
4 physical enclosure including a non-volatile memory that contains bus numbering
5 information for buses contained in the first and second physical enclosures; and
6 a bus number manager that detects a change in configuration of the computer
7 system and that reads the bus numbering information from the non-volatile memory for
8 the first and second physical enclosures to determine an appropriate bus number for at
9 least one bus in the first and second physical enclosures.

1 5. (Original) The computer system of claim 4 wherein the bus numbering
2 information comprises a beginning bus number and a number of buses.

1 6. (Original) The computer system of claim 4 wherein the non-volatile memory
2 comprises at least one identifier that is read by the bus number manager to
3 determine if contents of the non-volatile memory are valid.

1 7. (Original) A computer system comprising:
2 (1) a first physical enclosure comprising:
3 at least one processor;
4 a memory coupled to the at least one processor;
5 a non-volatile memory coupled to the at least one processor, the non-
6 volatile memory including a bus number mask that indicates bus numbers in use
7 in the computer system; and
8 a hub coupled to the at least one processor;
9 (2) a second physical enclosure comprising:
10 at least one bridge coupled to the hub in the first physical enclosure;
11 at least one numbered bus coupled to the at least one bridge;
12 a non-volatile memory that contains:
13 (A) bus numbering information for numbered buses in the second
14 physical enclosure; and
15 (B) bus numbering information for numbered buses in a third
16 physical enclosure;
17 (3) the third physical enclosure comprising:
18 at least one bridge coupled to the at least one bridge in the second physical
19 enclosure;
20 at least one numbered bus coupled to the at least one bridge in the third
21 physical enclosure;
22 a non-volatile memory that contains:
23 (A) bus numbering information for numbered buses in the third
24 physical enclosure; and
25 (B) bus numbering information for numbered buses in the second
26 physical enclosure;

(claim 7 continued)

27 (4) a bus number manager residing in the memory of the first physical enclosure
28 and executed by the at least one processor in the first physical enclosure, the bus number
29 manager detecting a change in configuration of the computer system and reading the bus
30 numbering information from the non-volatile memory in the second and third physical
31 enclosures to determine an appropriate bus number for at least one bus in the second and
32 third physical enclosures.

1 8. (Original) The computer system of claim 7 wherein the bus numbering
2 information comprises a beginning bus number and a number of buses.

1 9. (Original) A computer-implemented method for storing bus numbering
2 information in a non-volatile memory, the method comprising the steps of:
3 assigning unique bus numbers to buses in a first physical enclosure;
4 assigning unique bus numbers to buses in a second physical enclosure; and
5 storing the bus numbers for the buses in the first and second physical enclosures
6 in the non-volatile memory.

1 10. (Original) The method of claim 9 wherein the non-volatile memory resides in
2 the first physical enclosure.

1 11. (Original) The method of claim 9 wherein the bus numbering information
2 comprises a beginning bus number and a number of buses.

1 12. (Original) A computer-implemented method for numbering a plurality of
2 buses in a computer system that includes a plurality of physical enclosures, the
3 method comprising the steps of:
4 storing in a non-volatile memory bus numbering information for at least one bus
5 in a first physical enclosure;
6 storing in the non-volatile memory bus numbering information for at least one bus
7 in a second physical enclosure;
8 detecting a change in the computer system configuration; and
9 reading the bus numbering information from the non-volatile memory for the first
10 and second physical enclosures to determine an appropriate bus number for at least one
11 bus in the first and second physical enclosures.

1 13. (Original) The method of claim 12 wherein the bus numbering information
2 comprises a beginning bus number and a number of buses.

1 14. (Original) A computer-implemented method for assigning and maintaining
2 persistent numbers to a plurality of buses in a computer system that includes a
3 plurality of physical enclosures, the method comprising the steps of:
4 assigning unique bus numbers to buses in a first physical enclosure;
5 assigning unique bus numbers to buses in a second physical enclosure coupled to
6 the first physical enclosure;
7 storing bus numbering information corresponding to the bus numbers for the
8 buses in the first and second physical enclosures in a first non-volatile memory in the first
9 physical enclosure;
10 storing bus numbering information corresponding to the bus numbers for the
11 buses in the first and second physical enclosures in a second non-volatile memory in the
12 second physical enclosure;
13 detecting a change in the computer system configuration;
14 reading the bus numbering information from the first and second non-volatile
15 memories to determine an appropriate bus number for the buses in the first physical
16 enclosure; and
17 reading the bus numbering information from the first and second non-volatile
18 memories to determine an appropriate bus number for the buses in the second physical
19 enclosure.

1 15. (Original) The method of claim 14 wherein the bus numbering information
2 comprises a beginning bus number and a number of buses.

1 16. (Currently Amended) A program product comprising:
2 a bus number manager that detects a change in configuration of a computer
3 system that includes a plurality of physical enclosures, the bus number manager reading
4 bus numbering information from a non-volatile memory in a first physical enclosure to
5 determine an appropriate bus number for at least one bus in the first physical enclosure
6 and at least one bus in a second physical enclosure; and
7 [computer readable] recordable signal bearing media bearing the bus number
8 manager.

1 17. (Cancelled)

1 18. (Cancelled)

1 19. (Original) The program product of claim 16 wherein the bus numbering
2 information comprises a beginning bus number and a number of buses.

1 20. (Original) The program product of claim 16 wherein the non-volatile memory
2 comprises at least one identifier that is read by the bus number manager to
3 determine if contents of the non-volatile memory are valid.

STATUS OF THE CLAIMS

Claims 1-20 were originally filed in this patent application. In this amendment, claim 16 has been amended and claims 17-18 have been cancelled. Claims 1-16, 19 and 20 are currently pending.

In the pending office action claims 16-20 were rejected under 35 U.S.C. §101 because they are not limited to tangible embodiments. Claims 1-17 and 19-20 were rejected under 35 U.S.C. §103(a) as being unpatentable over Applicant's Admitted Prior Art (AAPA) in view of Mizukami. No claim was allowed.